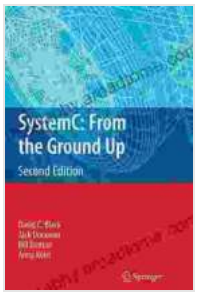
Master SystemC: A Comprehensive Guide to Modeling and Simulation

SystemC is a powerful modeling and simulation language designed specifically for system-level design. It enables engineers to create accurate and detailed models of complex hardware systems, including processors, memories, buses, and peripherals. These models can be simulated to verify functionality, explore design alternatives, and optimize performance.



SystemC: From the Ground Up, Second Edition

4 out of 5

Language	: English
File size	: 13689 KB
Text-to-Speech	: Enabled
Screen Reader	: Supported
Enhanced typesetting	: Enabled
Print length	: 474 pages

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This comprehensive guide provides a thorough to SystemC, covering all aspects of the language from the ground up. Whether you're a beginner or an experienced designer, this book will help you develop the skills you need to model and simulate complex systems with confidence.

What's New in the Second Edition

The second edition of *SystemC from the Ground Up* has been fully updated to cover the latest version of the SystemC standard (SystemC 2.3.3). It also includes new chapters on:

- SystemC-AMS
- SystemC for FPGA-based design
- SystemC for high-level synthesis

In addition, the book has been expanded to include more examples and exercises, making it an even more valuable resource for SystemC developers.

Who Should Read This Book

This book is ideal for:

- Hardware engineers
- System architects
- Software developers
- Students

who want to learn how to model and simulate complex systems with SystemC.

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About the Author

Vinayak Prabhu is a leading expert in SystemC modeling and simulation. He has over 15 years of experience in the field, and has worked on a wide range of projects, from small-scale embedded systems to large-scale SoC designs. Vinayak is the author of several books and articles on SystemC, and he is a frequent speaker at industry conferences.

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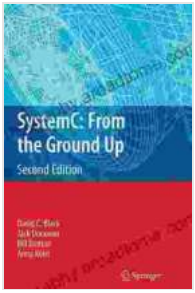

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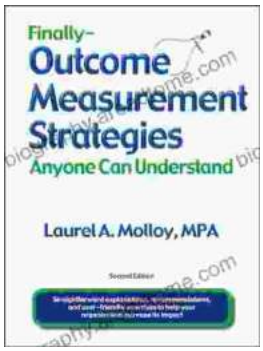

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